

Fig. 1

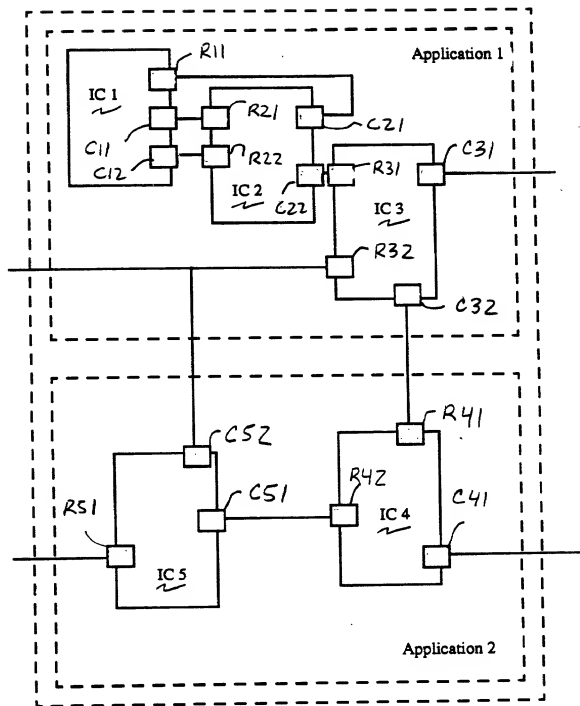
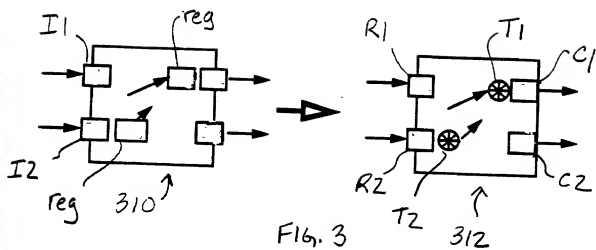


FIG. 2

Fig. 3 is a schematic diagram of a system 300 for processing two input signals I1 and I2. The system 300 includes a first processing block 310 and a second processing block 312. The first processing block 310 receives inputs I1 and I2 and produces two outputs. The second processing block 312 receives inputs R1 and R2 and produces two outputs C1 and C2. The outputs of the first processing block 310 are connected to the inputs of the second processing block 312. The first processing block 310 includes two registers (reg) and two comparators (represented by circles with a cross). The second processing block 312 includes two registers (reg) and two comparators (represented by circles with a cross). The system 300 is shown in a perspective view.



Connecting applications

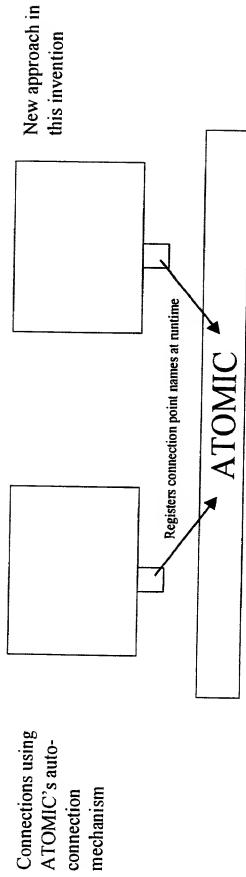
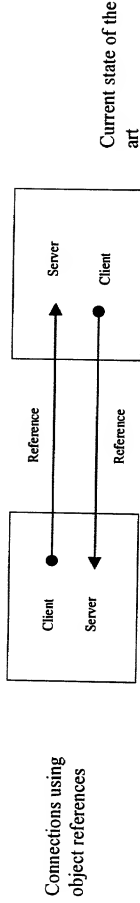


Figure 4

A sample use case

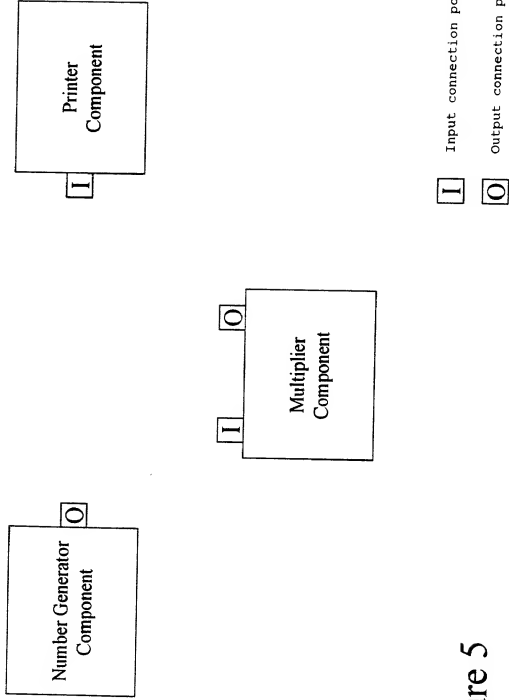


Figure 5

A sample use case

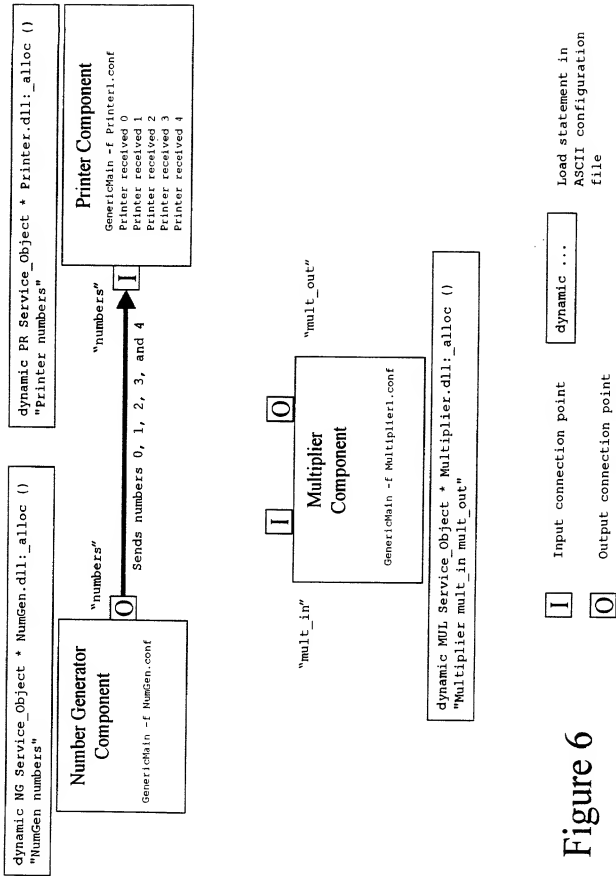


Figure 6

A sample use case

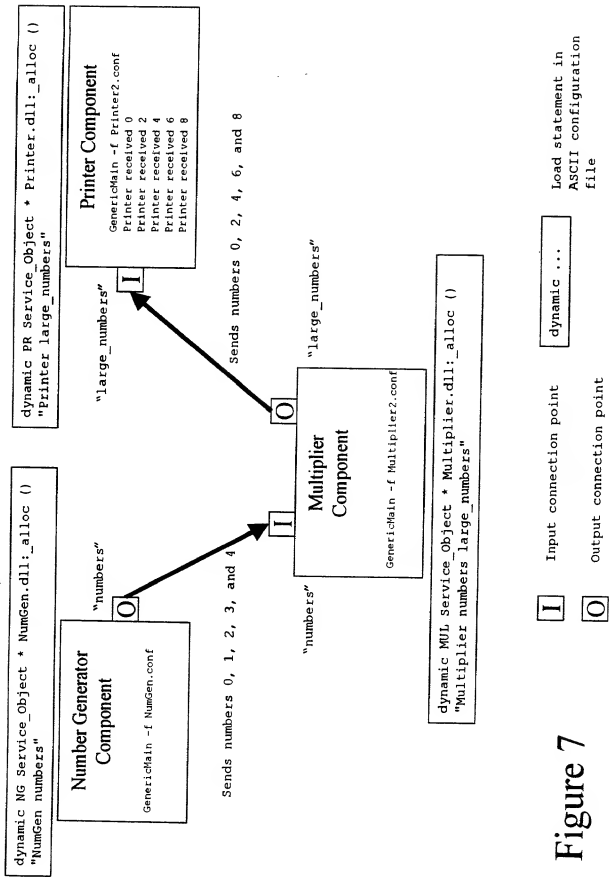


Figure 7